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538	206
Class	Subclass

ISSUE CLASSIFICATION

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PATENT NUMBER

**U.S. UTILITY Patent Application**

**O.L.P.E.**

**PATENT DATE**

**SCANNED**

## Q.A

~~JAN 13 2004~~

APPLICATION NO. 09/614113	CONT/PRIOR D	CLASS 257 438	SUBCLASS 7006	ART UNIT 2819 1765	EXAMINER Nep
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## APPLICANTS:

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Louie Liu  
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**TITLE**

Technique for elimination of pitting on silicon substrate during gate stack etch

PTO-2040  
12/99

12-24-03 Formal Drawings (14 sheets) set 1 10-9-03

### ISSUING CLASSIFICATION

ORIGINAL				CROSS REFERENCE(S)										
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)									
438		706		438	720	724	725							
INTERNATIONAL CLASSIFICATION														
11	0	1	2											

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg. 14	Figs. Drwg. 26	Print Fig. 6	Total Claims 8
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	DUY-VIL DEO 7/11/03 _____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b> <del>7/11/03</del>	
	Benjamin L. Utech <b>BENJAMIN L. UTECH</b> <b>SUPERVISORY PATENT EXAMINER</b> <b>TECHNOLOGY CENTER 1700</b> _____ (Primary Examiner) 7/21/03 (Date)		<b>ISSUE FEE</b> 60 Amount Due \$1700 <sup>as</sup> Date Paid <del>10-9-03</del>	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		<b>ISSUE BATCH NUMBER</b>	
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